Ashling announce availability of their new Vitra-XS Debug & Trace Probe

Dec-12, 2022 RISC-V Summit, San Jose, Silicon Valley, California, USA - Today Ashling announced availability of *Vitra-XS* their newest member of the Ashling probe family. *Vitra-XS* is a debug & trace probe for embedded development with support for multiple target architectures including RISC-V & Arm powered systems. *Vitra-XS* works with Ashling's *RiscFree™* Eclipse based IDE & Debugger and supports:

- Capturing (recording) & viewing of program-flow & data-accesses in real-time, non-intrusively
- Program downloading from the host PC to the target embedded system
- Exercising program in the target (go, step, halt, breakpoints, interrogate memory, registers & variables etc)

"Vitra-XS supports real-time trace which provides some key advantages when it comes to debugging and validating embedded systems. Using trace, developers can easily see how execution arrived at a certain point, via a back-trace or instruction history, and can answer questions like "How did I end up in this function?" and "Why did my code crash?" Trace information can be captured non-intrusively meaning that the application's real-time performance is not affected and allows developers to profile their code to find out where time is actually being spent and to determine if timing requirements are being met" said **Hugh O'Keeffe, CEO of Ashling**.

Vitra-XS features include:
- Fast, trouble-free "plug-&-play" installation using SuperSpeed USB3.0
- Supports RISC-V debug & trace standards including E-Trace & N-Trace
- Supports Arm CoreSight™ debug & trace standards including SWD, DAP, ETM, PTM, STM & CTI
- Heterogeneous (e.g. Arm + RISC-V) & homogeneous debug & trace support for multi-core SoCs sharing a single debug & trace interface
- 38-way Mictor target connector support with adapter support for other connector types
- Detects & automatically configures for the appropriate target voltage (from 0.9V to 5.0V)
- Automatic trace clock & data skew adjustment ("AUTOLOCK") to ensure integrity of captured high-speed data. *Vitra-XS* automatically calibrates itself to your target's trace data port
- Parallel (up to 16-bits data & additional control) trace capture up to 400MHz
- On-board trace storage memory (up to 512MB) which may be configured as a circular buffer to allow continuous trace capture up to a defined event (e.g. a breakpoint or exception)
- Unlimited trace storage supported via high-speed, real-time streaming over SuperSpeed USB to host PC hard disk
- Optional Gigabit Ethernet interface & compact form factor

*The Ashling **Vitra-XS** will be showcased for the first time at the RISC-V 2022 Summit in San Jose so come along and see it for yourself first hand at the Ashling silver booth (S3).*

About Ashling

Ashling have been a leading provider of Embedded Development Tools & Services since 1982 with design centres in Limerick Ireland and Cochin India and sales and support offices in Europe, Asia Pacific, the Middle East and America. Visit www.ashling.com for more details.

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