Ashling exhibits at Beijing Synopsys ARC® Summit
15th November, 2017 – BEIJING, CHINA.

Ashling showcased its latest high performance debug tools for ARC® processors at the Synopsys ARC® Processor Summit in Beijing, together with its local partner Emdoor Electronic & Technology. This was an ideal forum for ARC® users to learn about the latest technologies and trends in embedded processor IP, software, programming tools and applications.

As a strategic partner of Synopsys, Ashling joined experts from Synopsys, other Synopsys partners and the broader ARC user community to discuss challenges and solutions for a variety of topics including IoT security, automotive safety, embedded vision, and much more.