



Ashling had a strong presence at the 8th RISC-V Workshop *RISC-V Community Gets a Turnkey Software Solution Via Ashling*

SPAIN, Barcelona May 11, 2018 – Ashling had a strong showing at the 8th RISC-V Workshop held in Barcelona. Ashling team along with the Imperas team presented a well-attended technical session. RISC-V community is now well aware of the Ashling ecosystem of tools supporting RISC-V.

“We are proud of what we have achieved so far in supporting the RISC-V marketplace and look forward to achieving more” said Dr. Guy Rabbat President and CEO of Ashling Systems Corporation.



The 8th RISC-V Workshop was held in Barcelona May 7-10 with 325 attendees and was the biggest event outside of Silicon Valley, demonstrating the momentum of the RISC-V Foundation and the growth of the ecosystem in Europe.

Member companies announced new solutions, partnerships, and participated in the workshops with presentations and active networking and exchange of information as befits an open-source organization committed to the common good and continuing RISC technology progress.

Barcelona Supercomputing Center (BSC) and Universitat Politècnica de Catalunya (UPC) co-hosted the Workshop, and NXP and Western Digital co-sponsored the successful event.

Ashling along with its Partner Imperas had a joint presentation entitled 'Virtual Platforms for Early Embedded Software Development' detailing how virtual platforms accelerate software porting and development and how using Ashling's RISC-V IDE can be used throughout the complete design cycle from simulation using Imperas models to device/board bring-up with actual silicon.

All RISC-V Workshop in Barcelona Proceedings - RISC-V Foundation can be viewed/downloaded [here](#)

Ashling's RiscFree™ IDE for RISC-V is now available directly from Ashling. For more information, visit www.ashling.com

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